

FIG. 1A

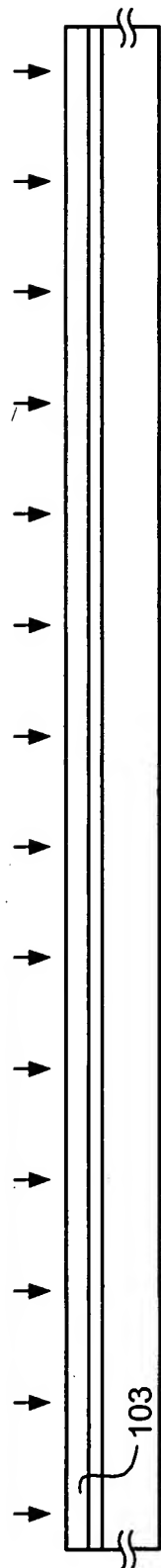


FIG. 1B

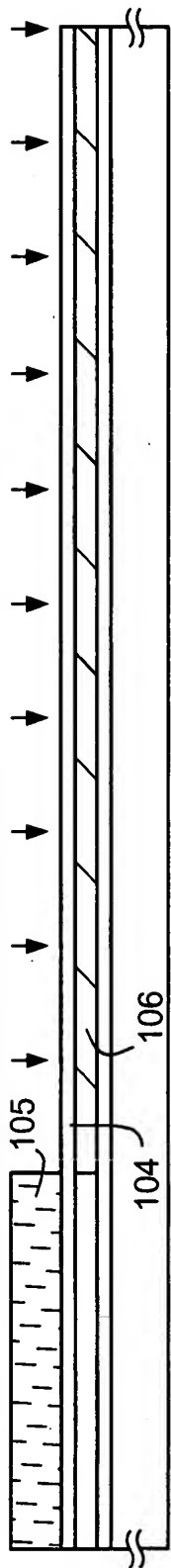


FIG. 1C

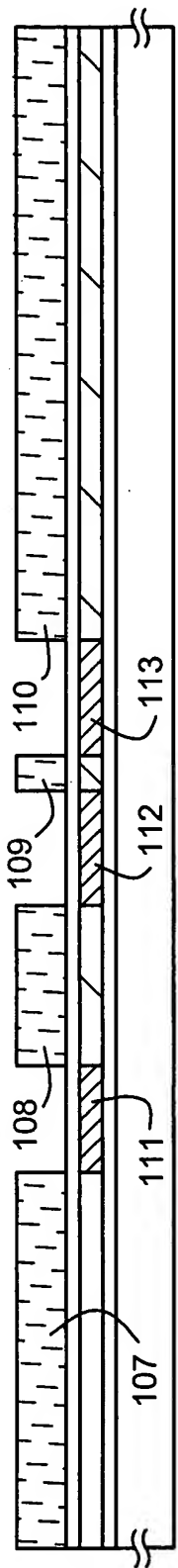


FIG. 1D

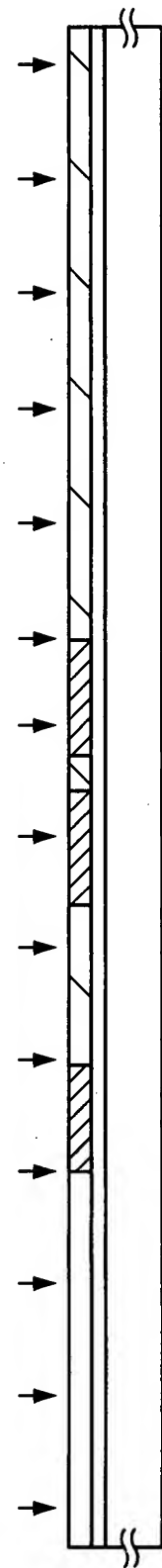


FIG. 1E

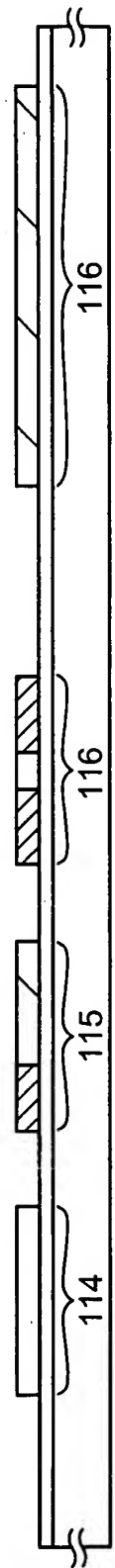
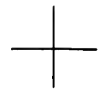


FIG. 1F



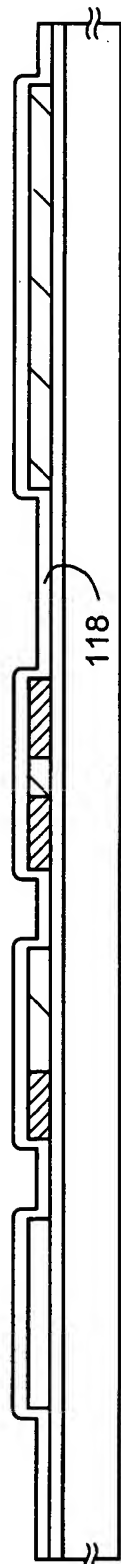


FIG. 2A

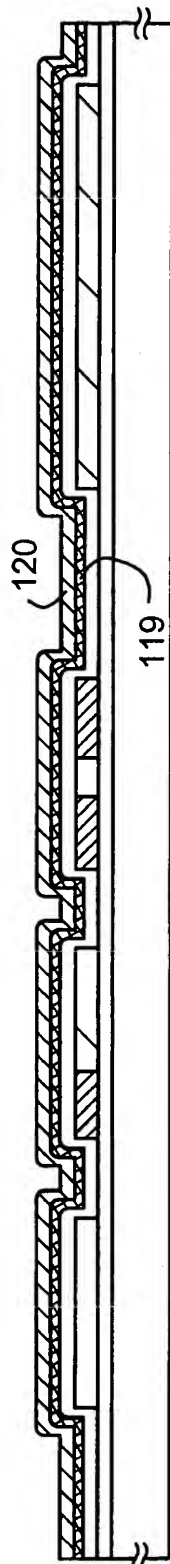


FIG. 2B

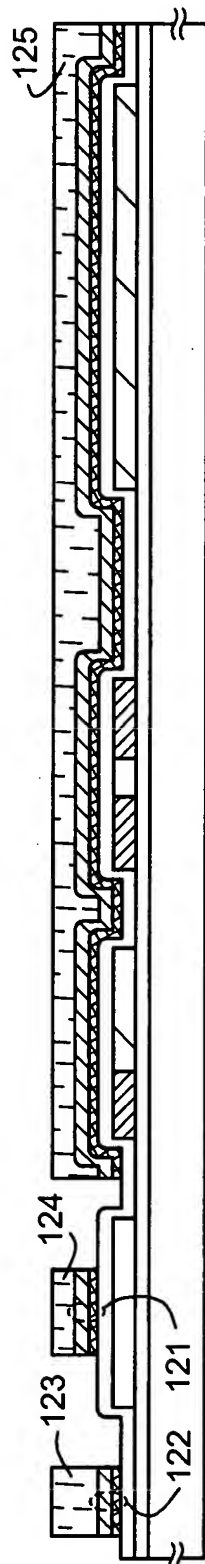


FIG. 2C

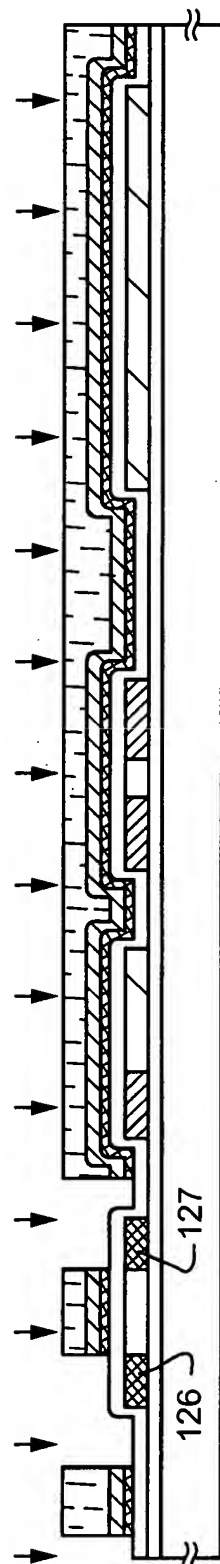


FIG. 2D

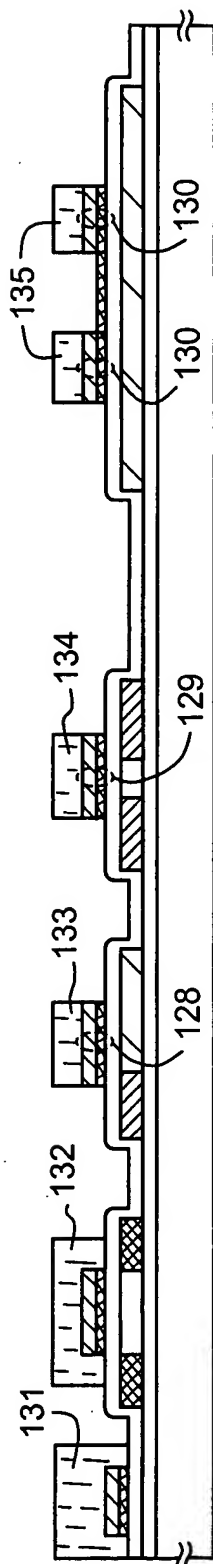


FIG. 2E



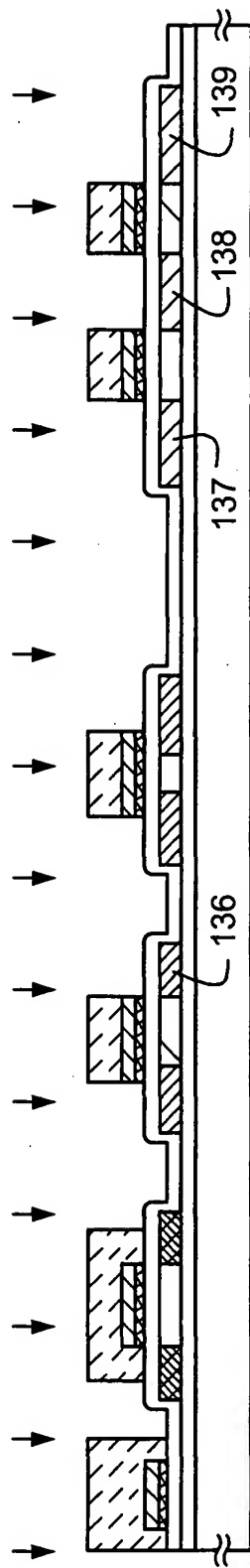


FIG. 3A

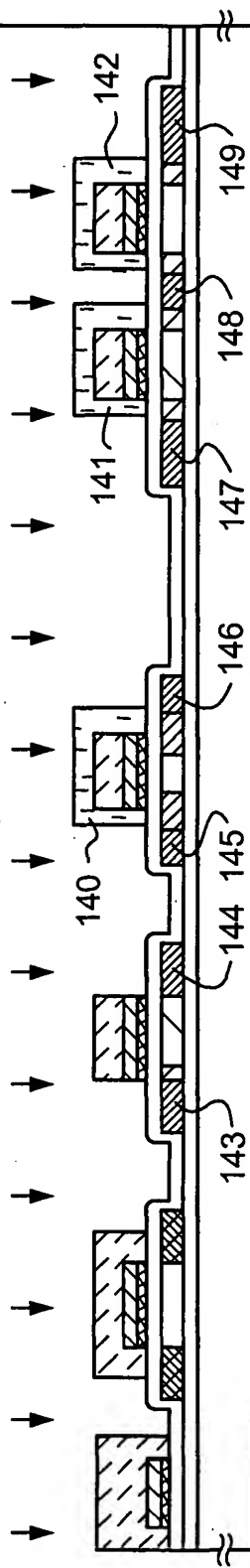


FIG. 3B

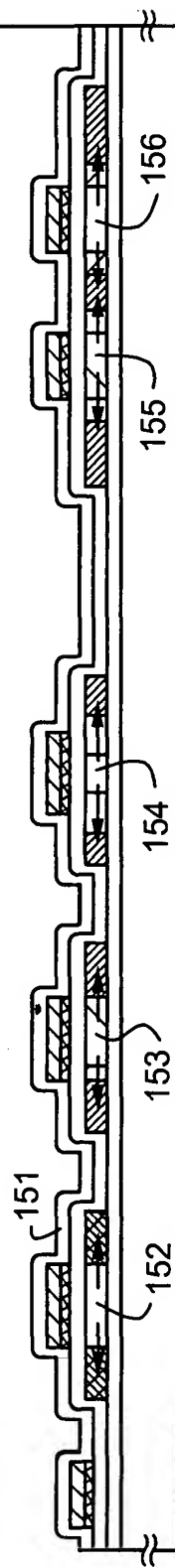


FIG. 3C



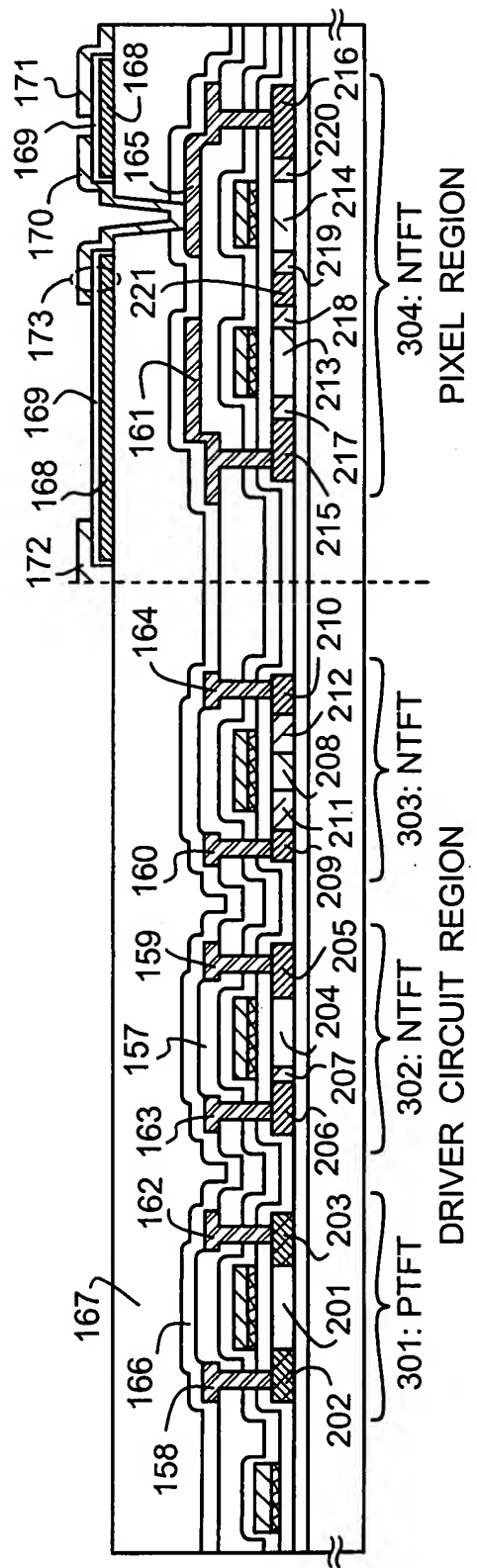


FIG. 4A

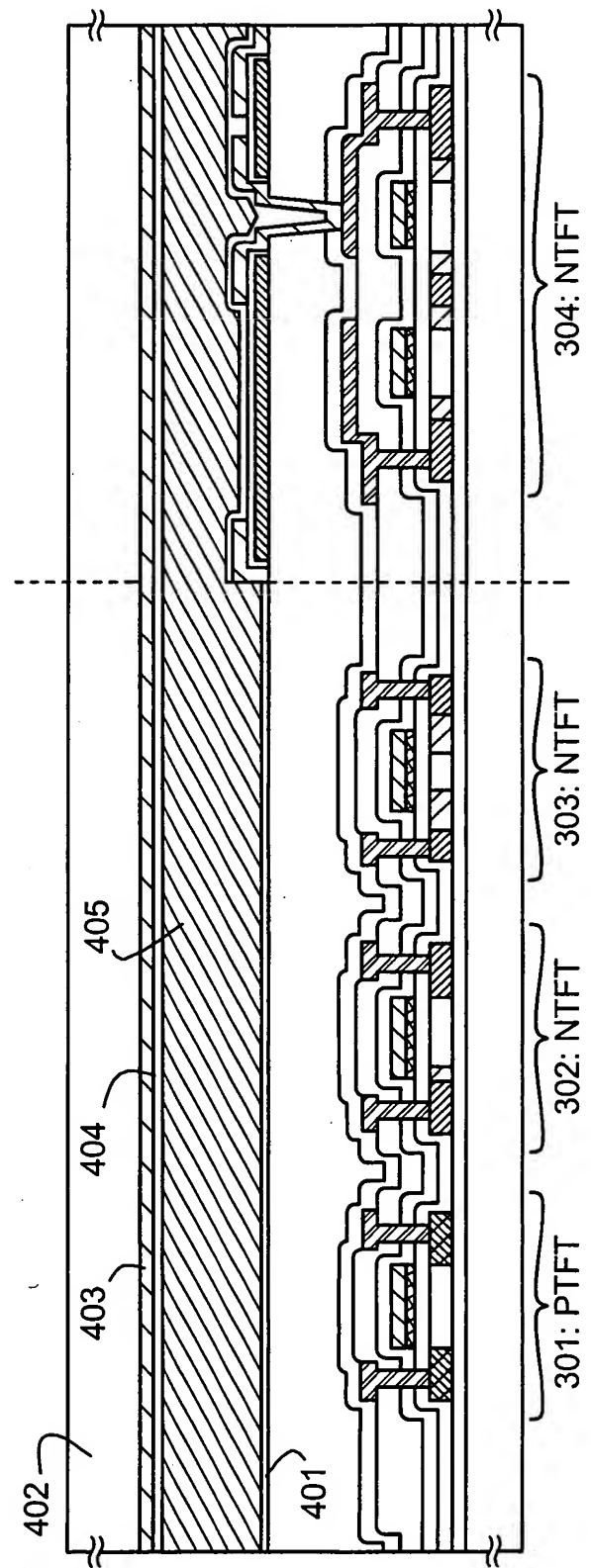
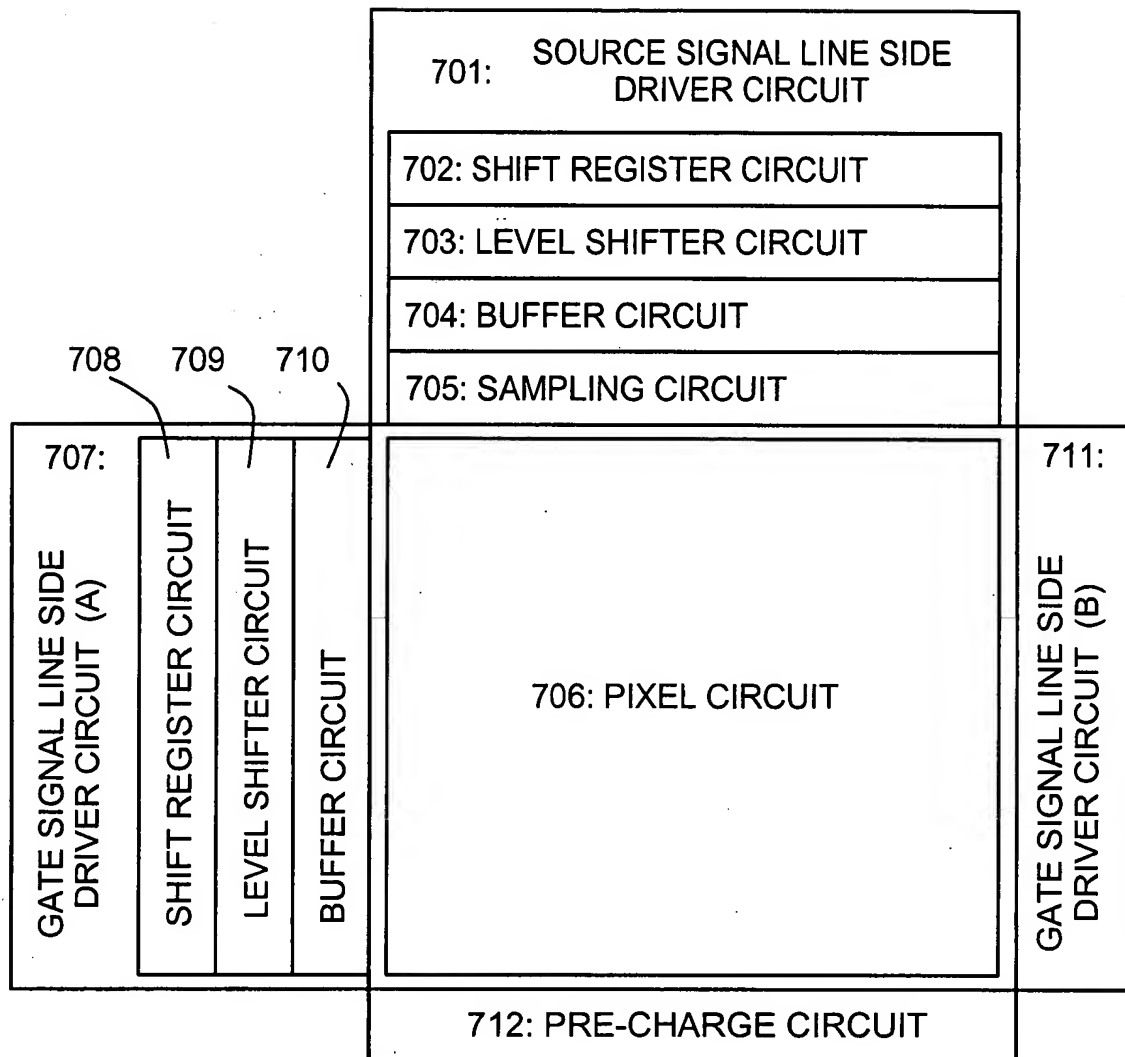


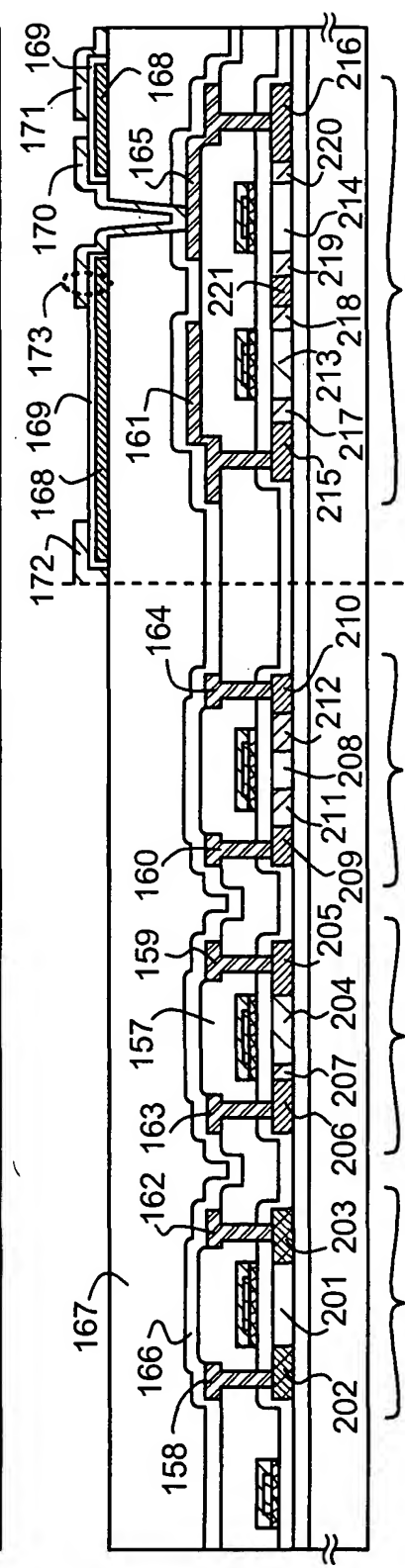
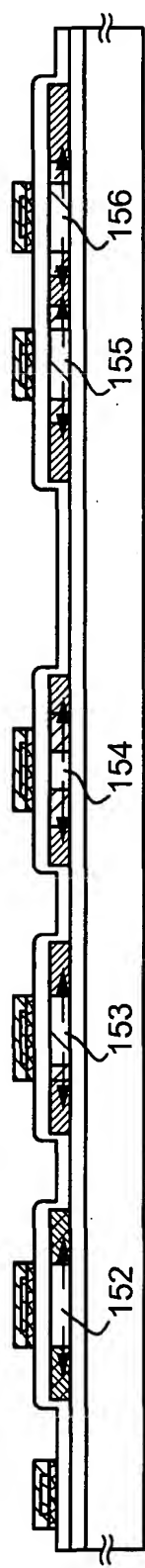
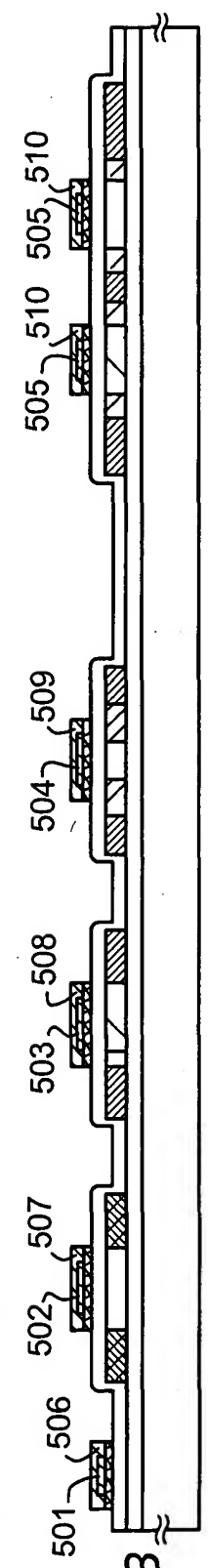
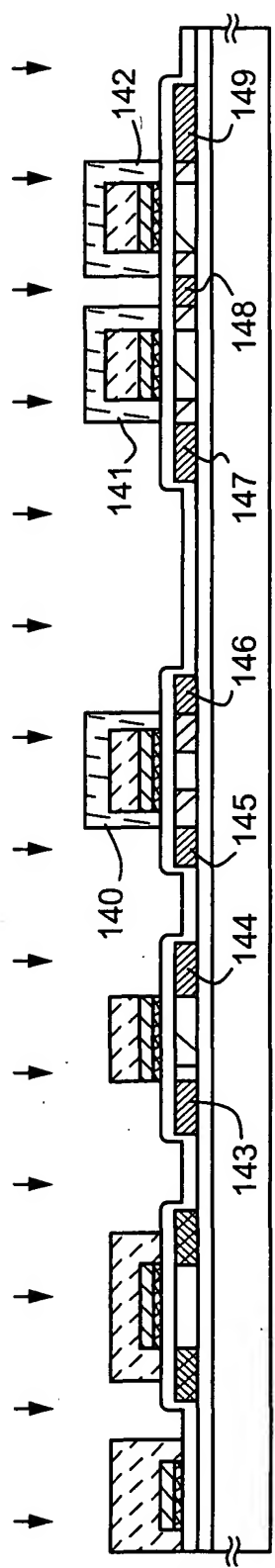
FIG. 4B







**FIG. 7**



301: PTFT      302: NTFT      303: NTFT      304: NTFT  
 DRIVER CIRCUIT REGION      PIXEL CIRCUIT REGION

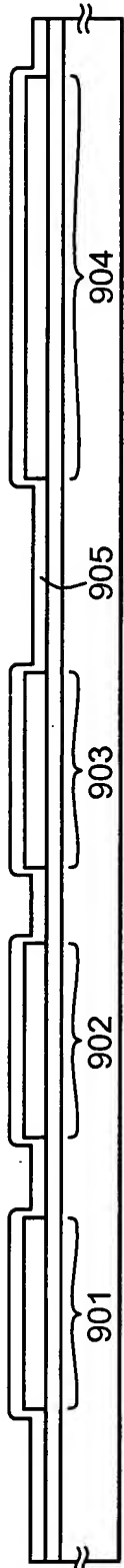


FIG. 9A

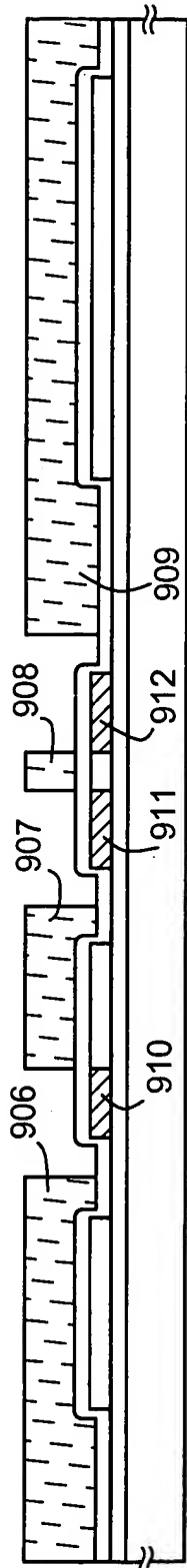


FIG. 9B

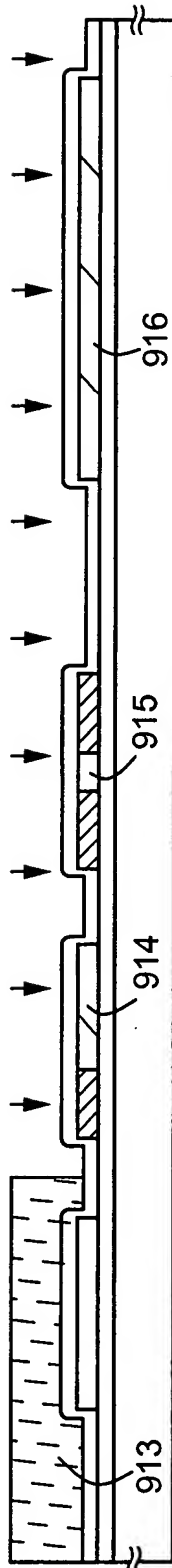


FIG. 9C

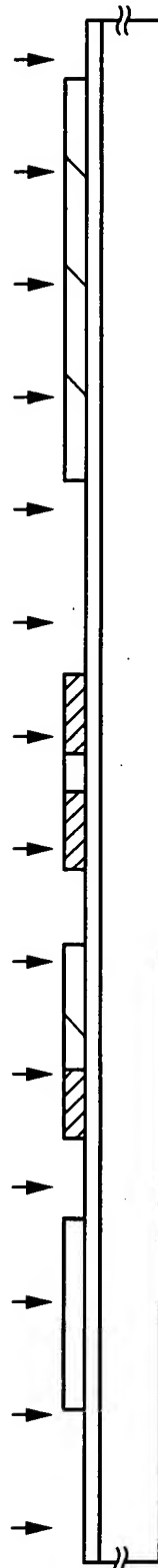


FIG. 9D





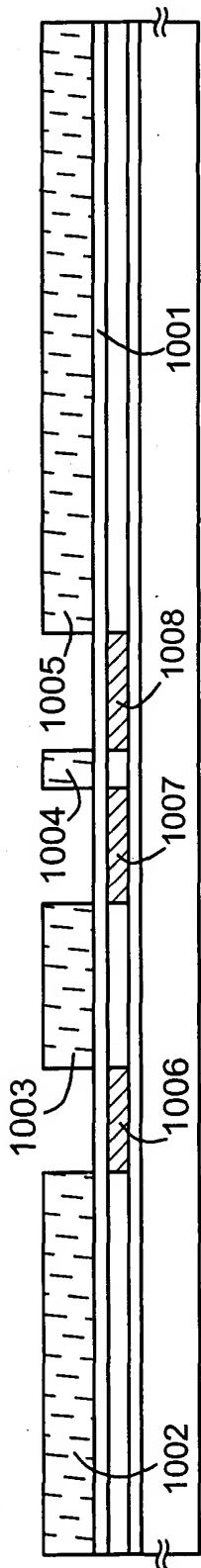


FIG. 10A

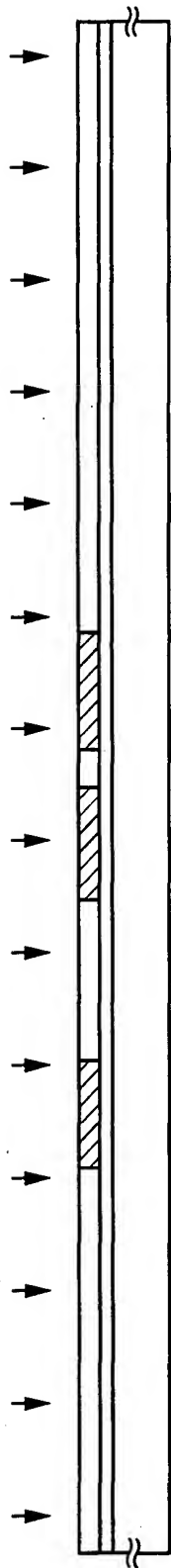


FIG. 10B

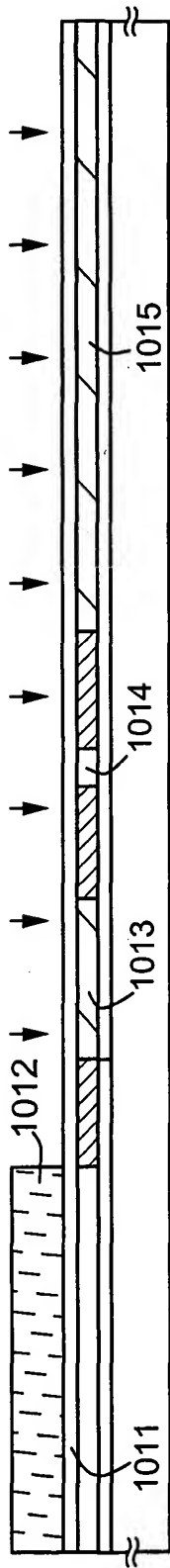


FIG. 10C

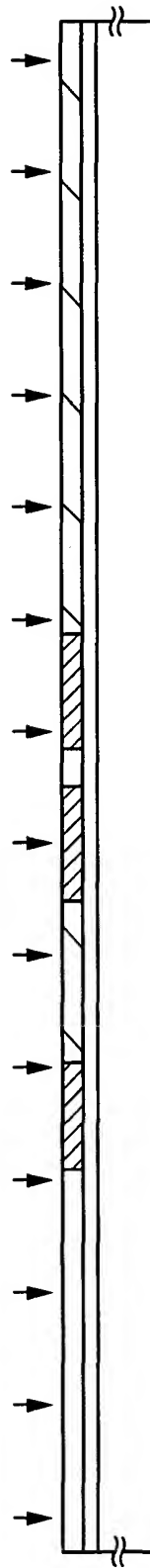


FIG. 10D



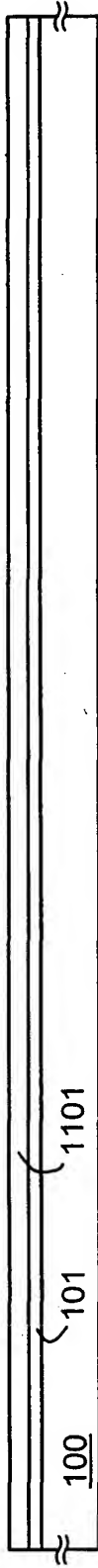


FIG. 11A

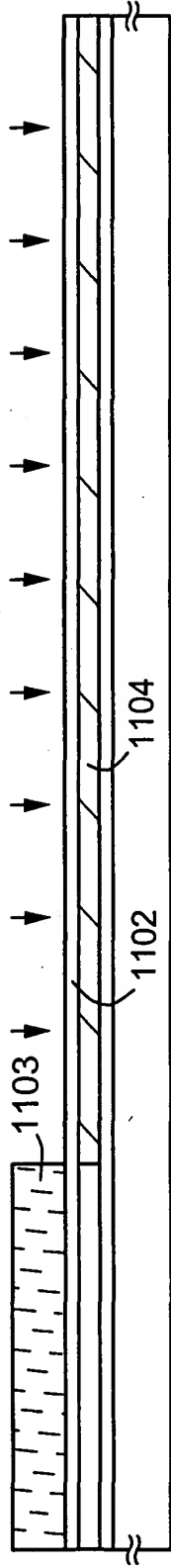


FIG. 11B

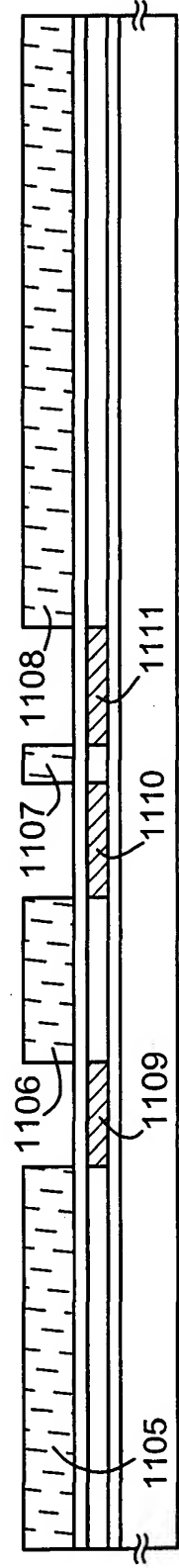


FIG. 11C

1112

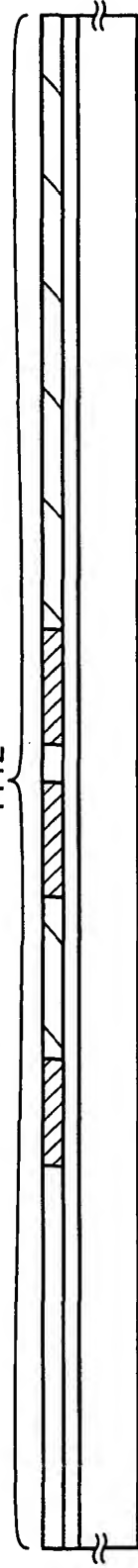


FIG. 11D

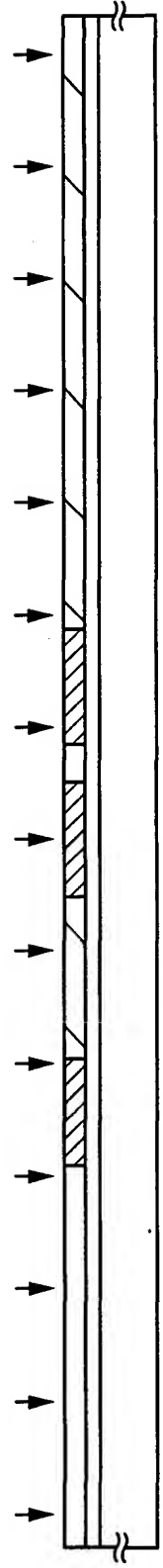


FIG. 11E





FIG. 12A

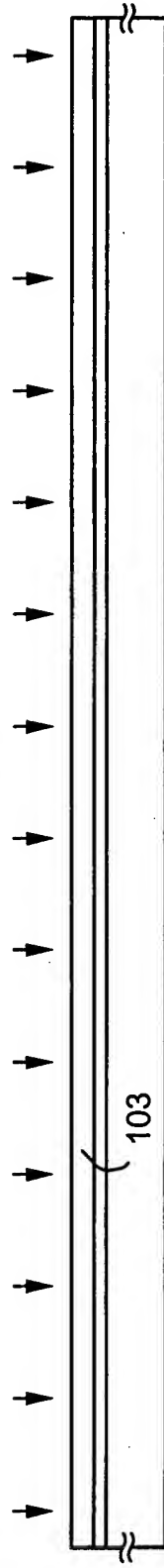


FIG. 12B

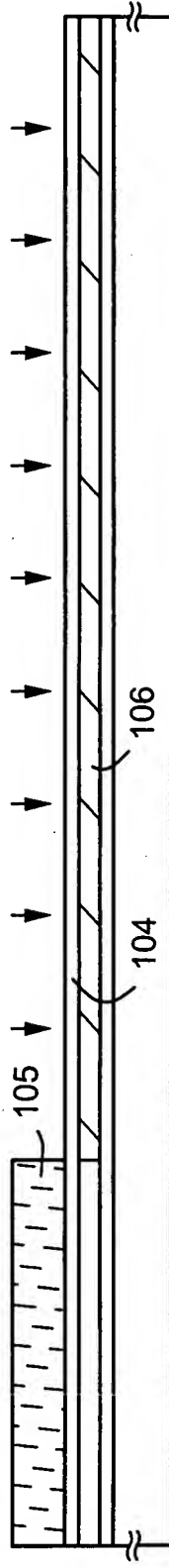


FIG. 12C

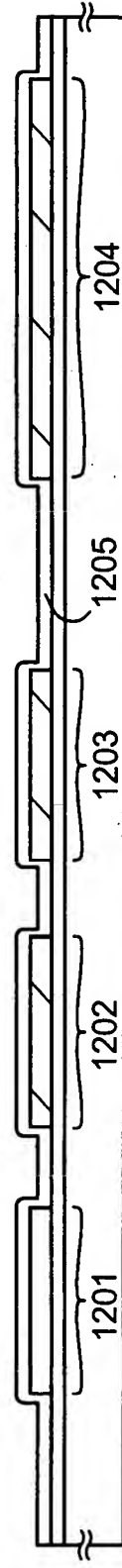


FIG. 12D

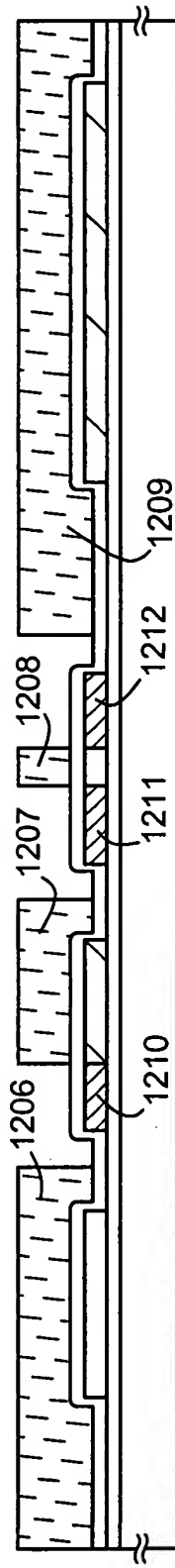


FIG. 12E

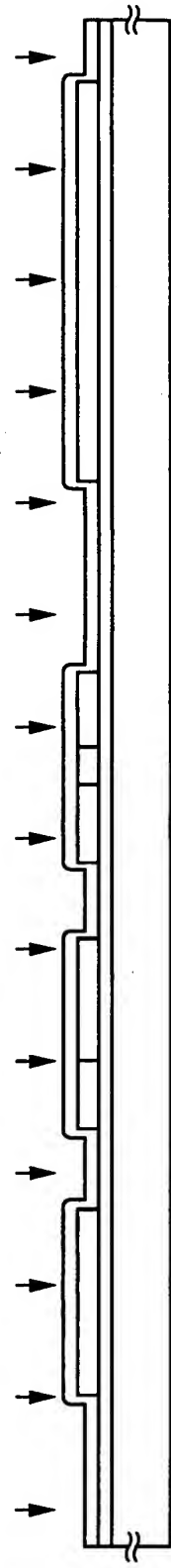


FIG. 12F





FIG. 13A

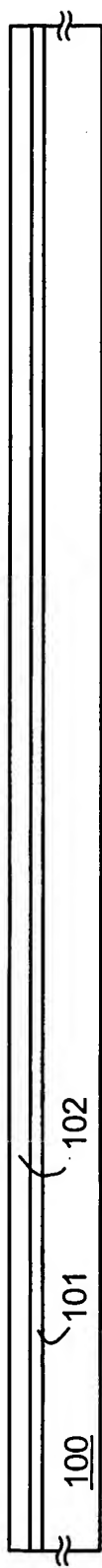


FIG. 13B

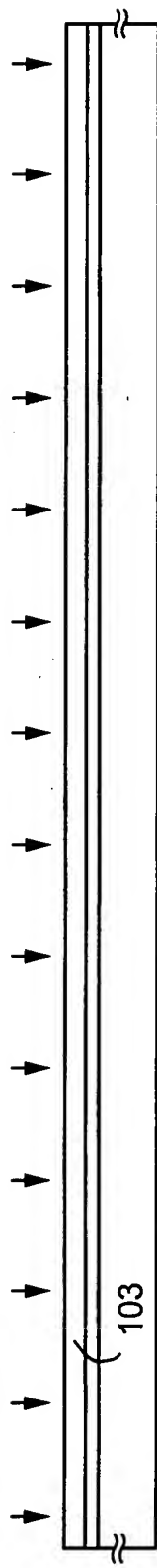


FIG. 13C

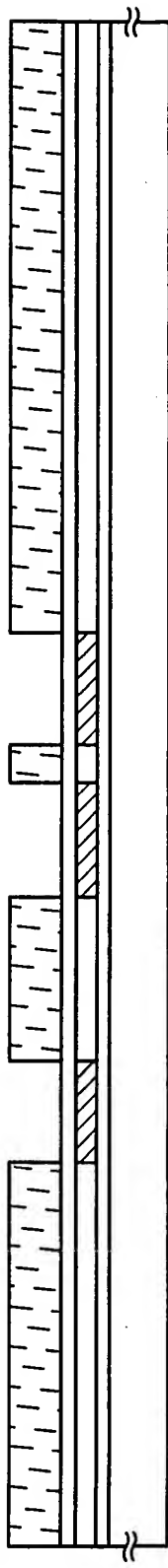


FIG. 13D

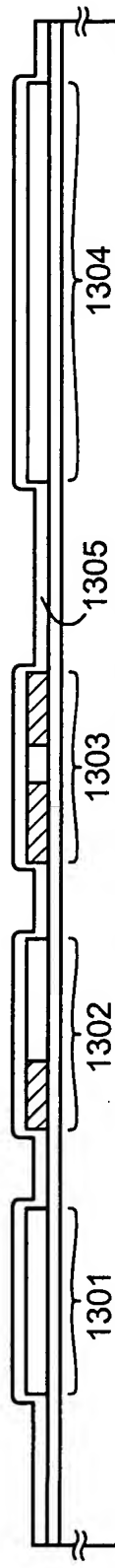


FIG. 13E

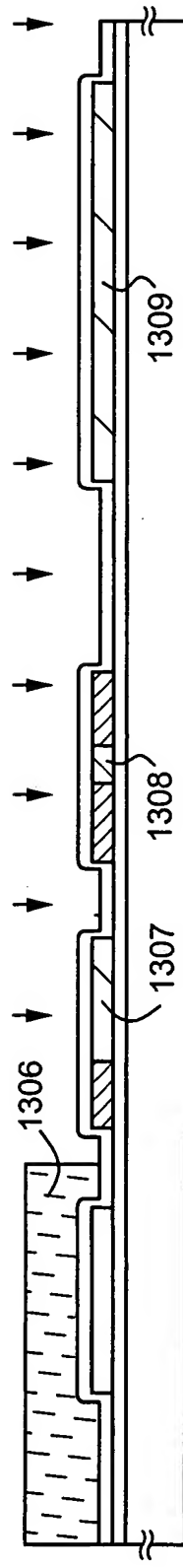
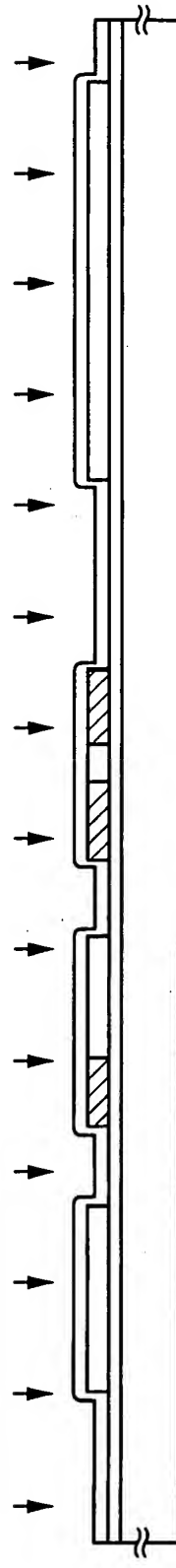


FIG. 13F



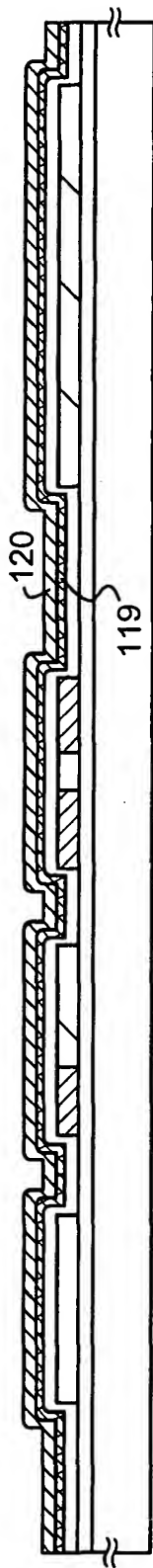


FIG.  
14A

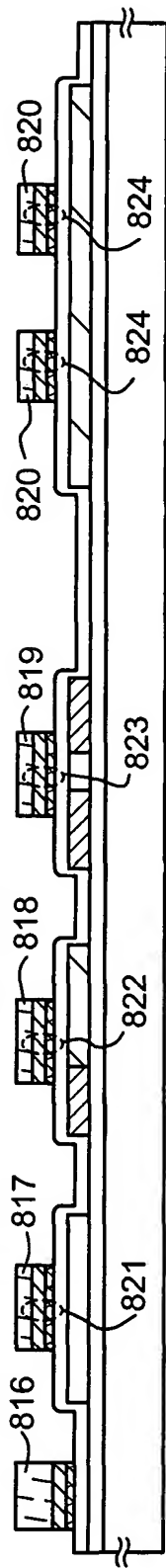


FIG.  
14B

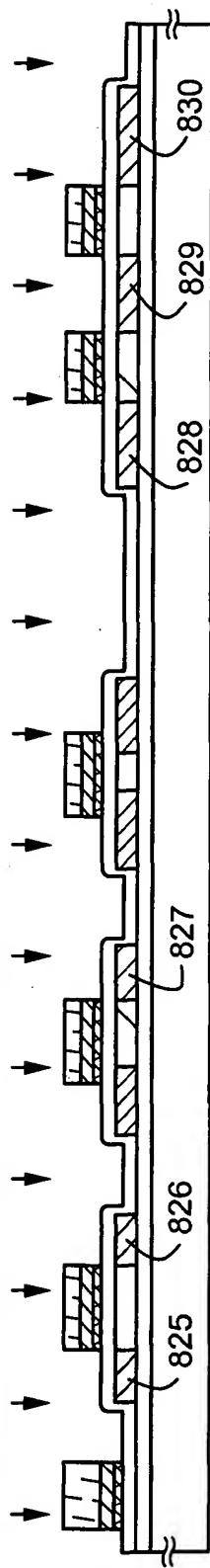


FIG.  
14C

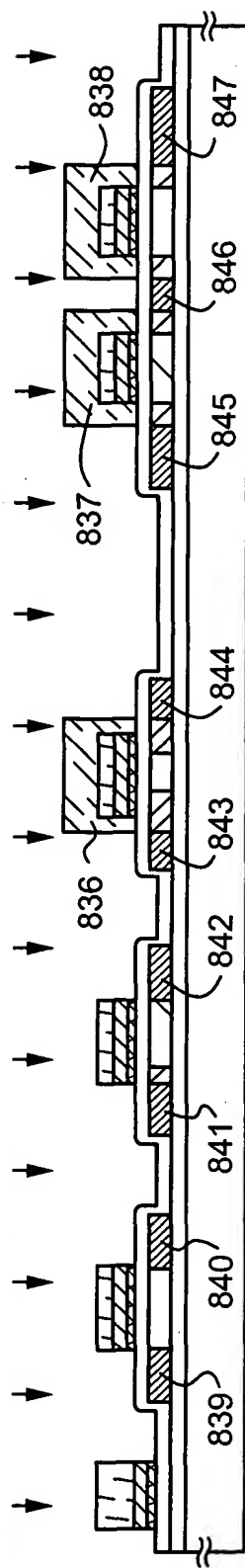


FIG.  
14D

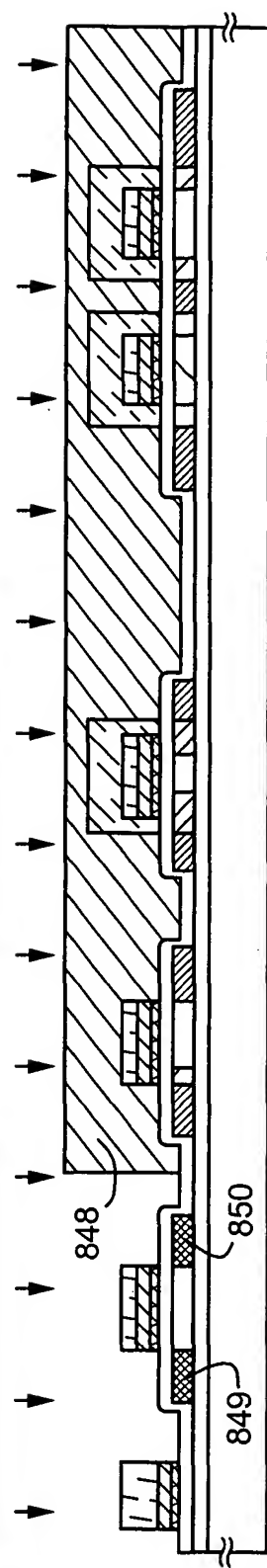


FIG.  
14E



FIG. 15A



FIG. 15B

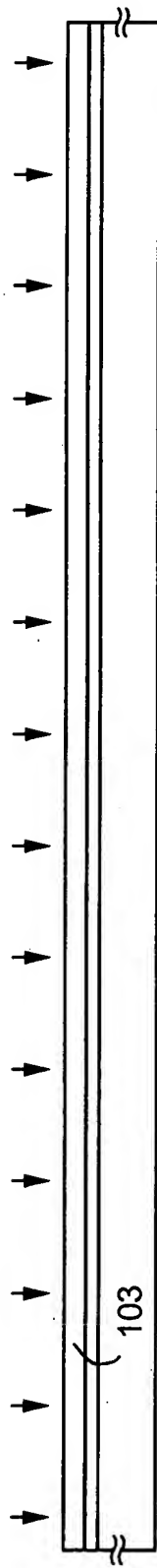


FIG. 15C

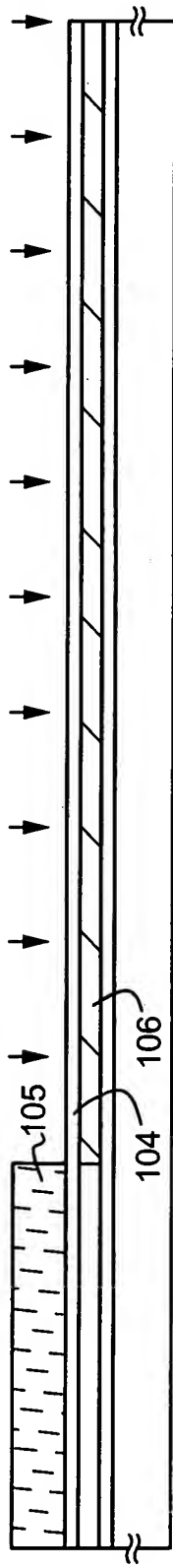


FIG. 15D

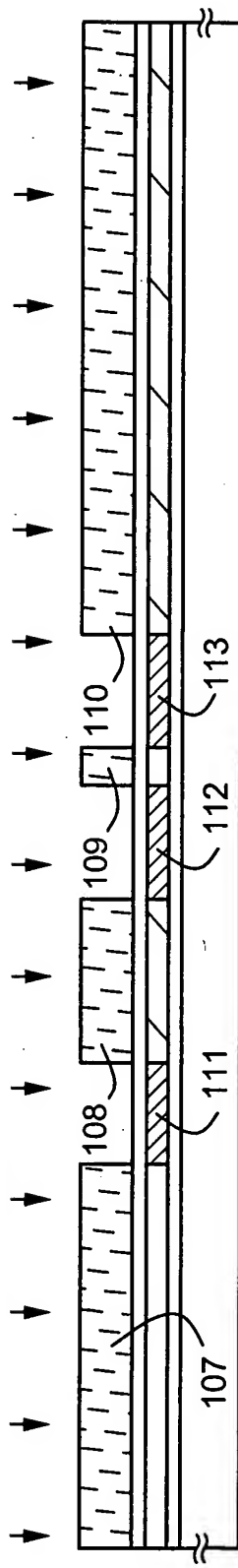
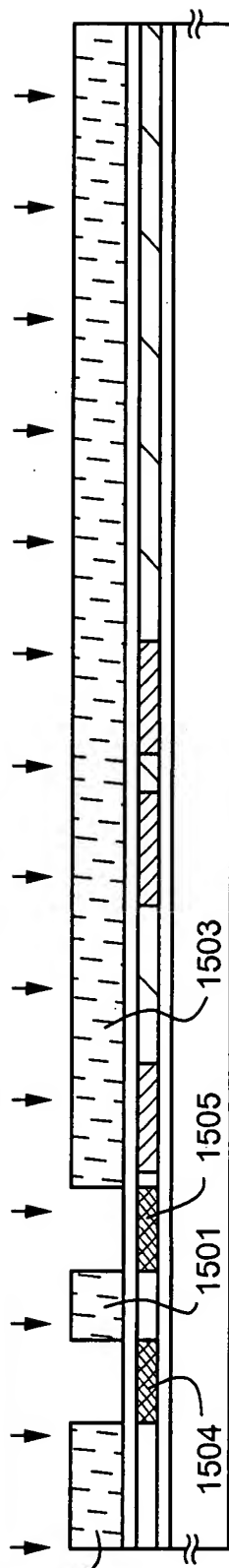


FIG. 15E



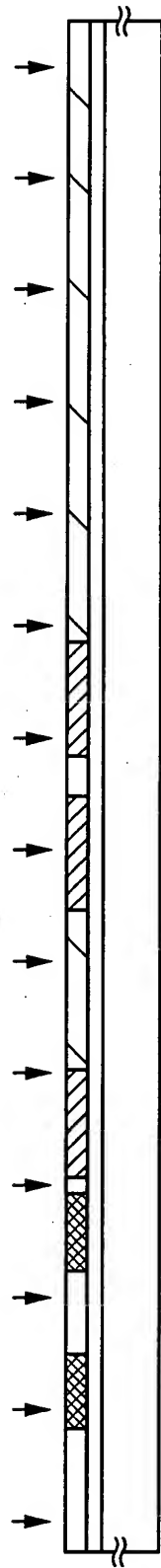


FIG.  
16A

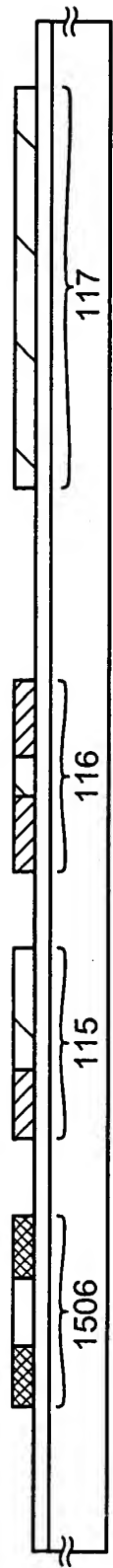


FIG.  
16B

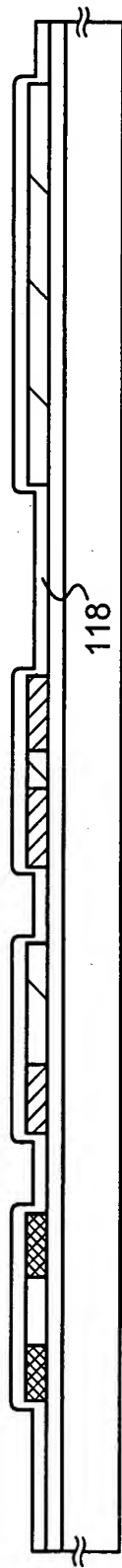


FIG.  
16C

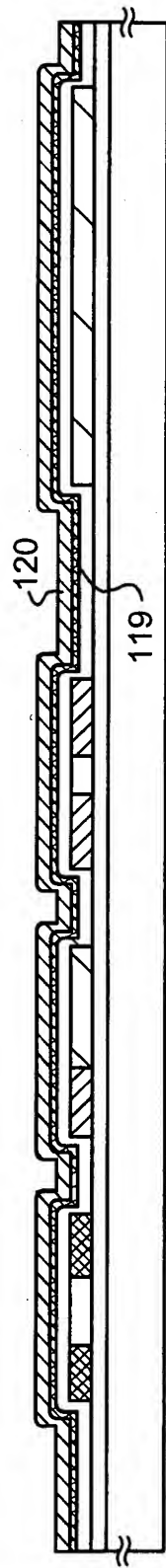


FIG.  
16D

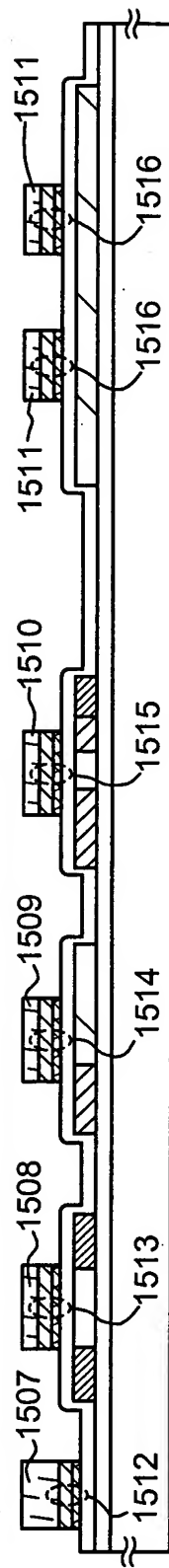


FIG.  
16E

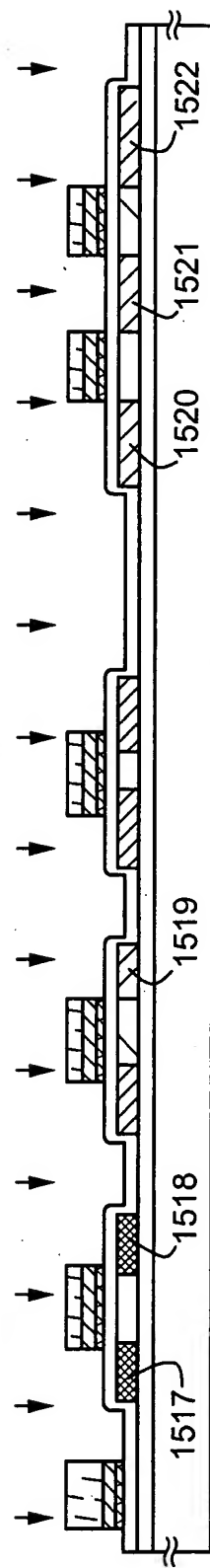


FIG.  
16F



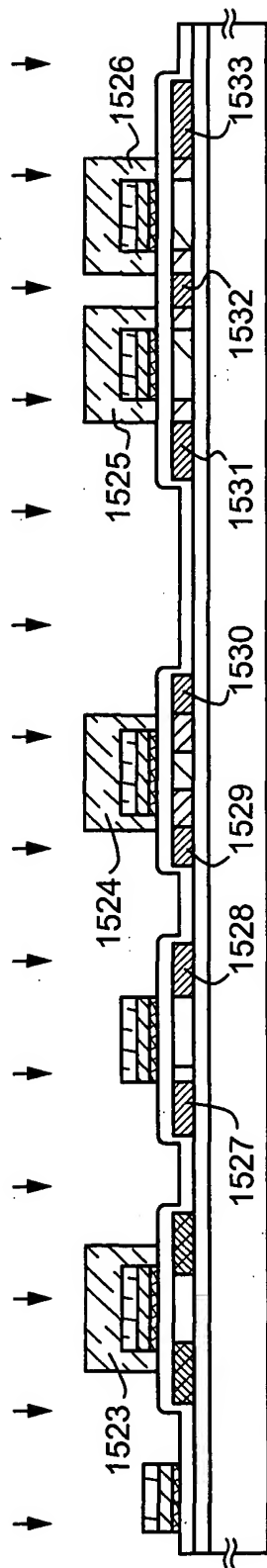


FIG. 17A

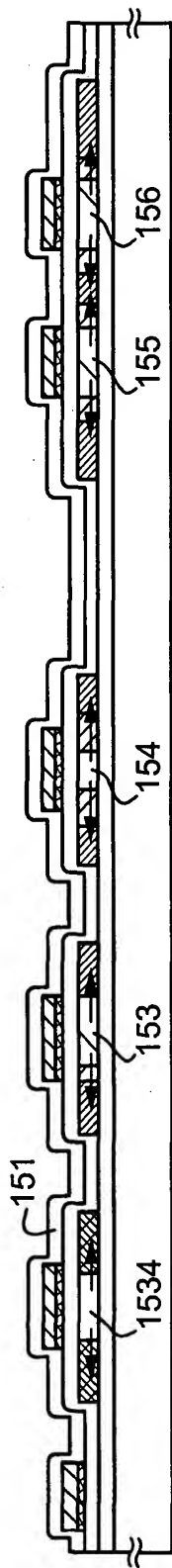


FIG. 17B

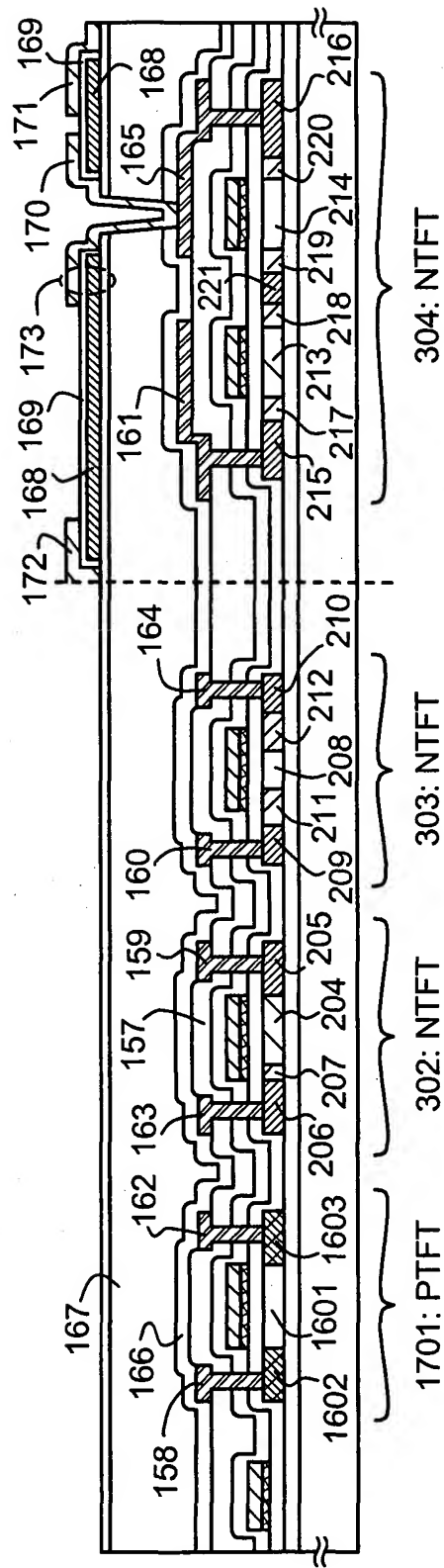


FIG. 17C



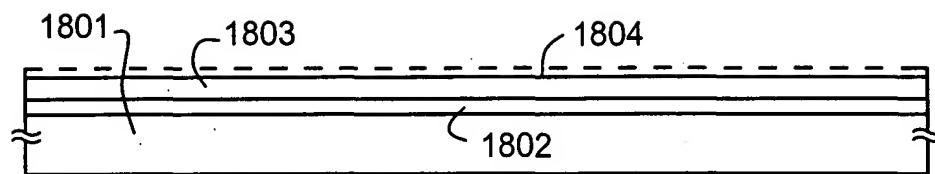


FIG. 18A

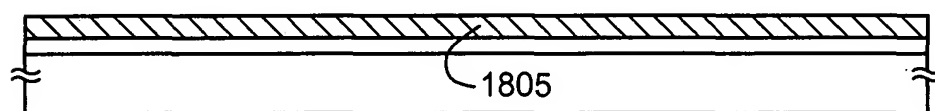


FIG. 18B

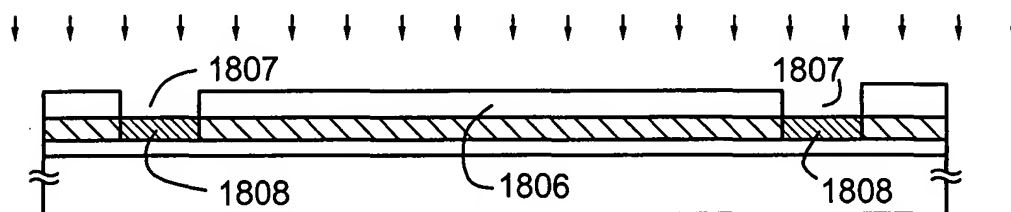


FIG. 18C

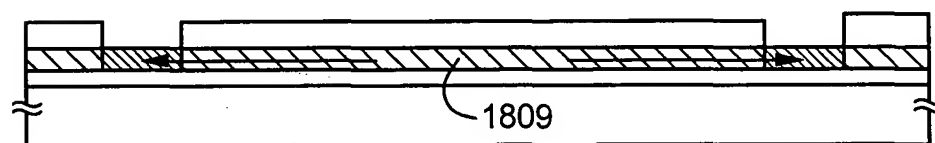


FIG. 18D

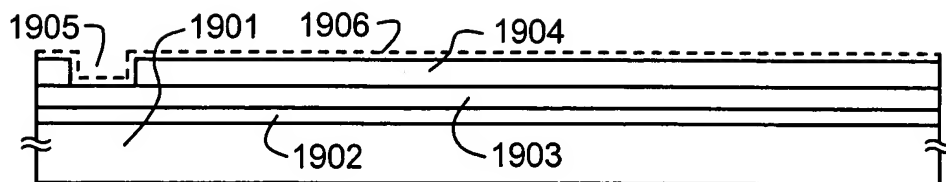


FIG. 19A

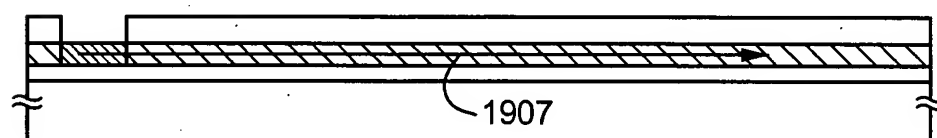


FIG. 19B

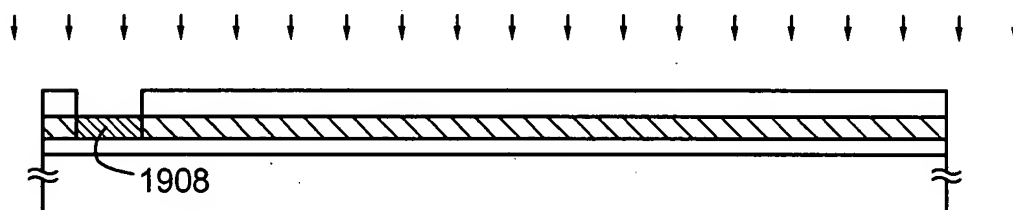


FIG. 19C

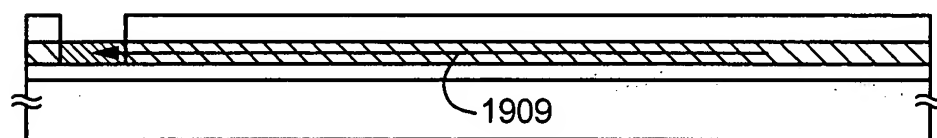


FIG. 19D

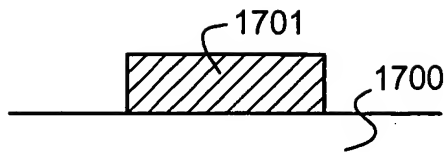


FIG. 20A

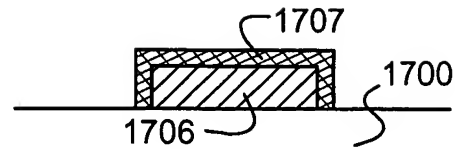


FIG. 20D

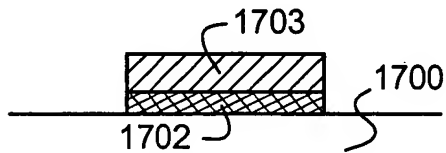


FIG. 20B

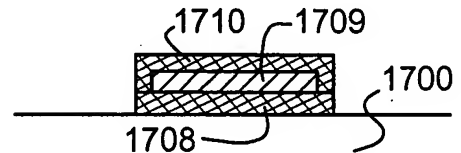


FIG. 20E

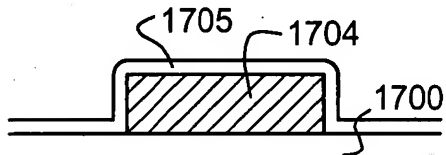


FIG. 20C

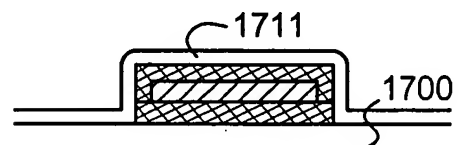


FIG. 20F

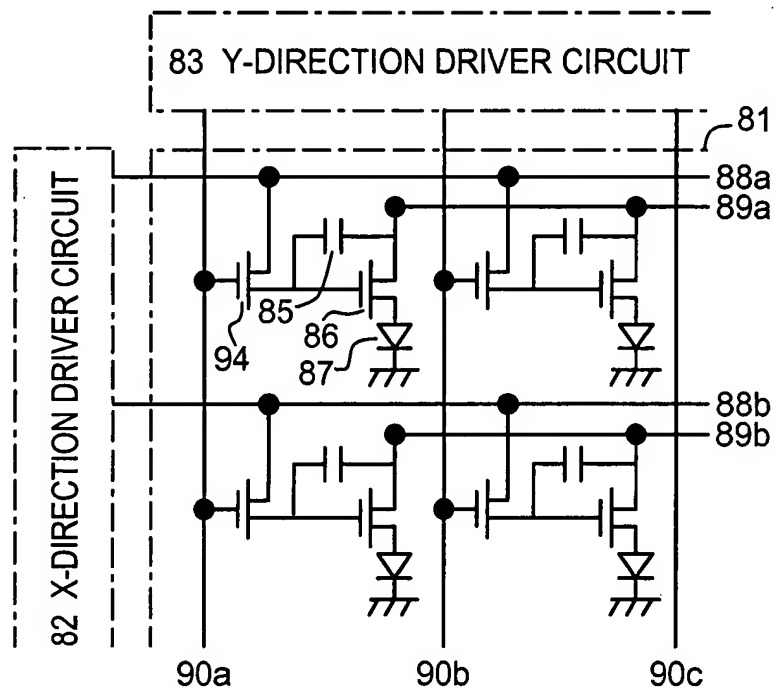


FIG. 21

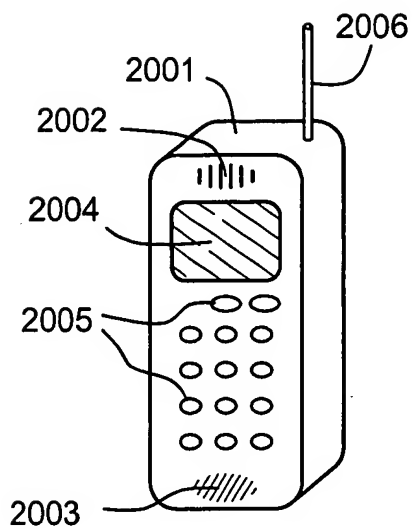


FIG. 22A

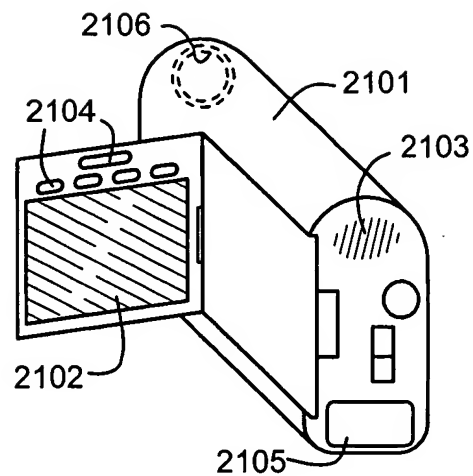


FIG. 22B

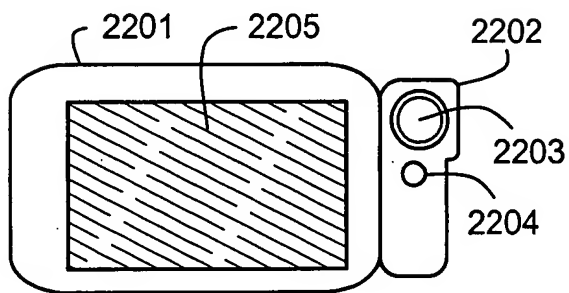


FIG. 22C

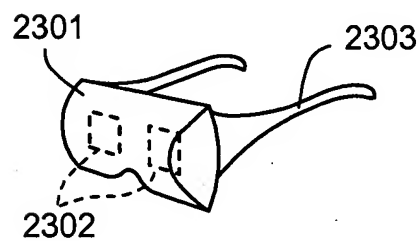


FIG. 22D

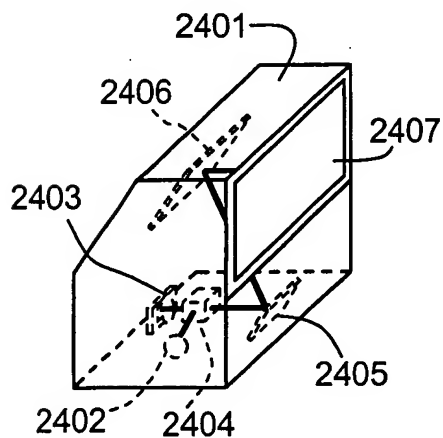


FIG. 22E

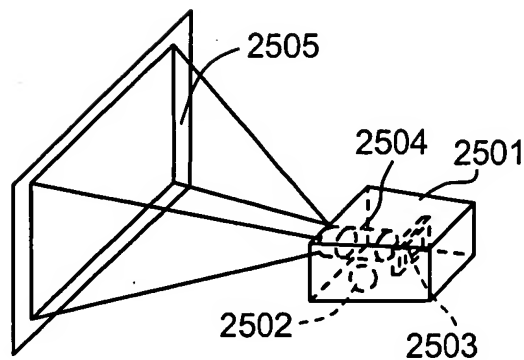


FIG. 22F

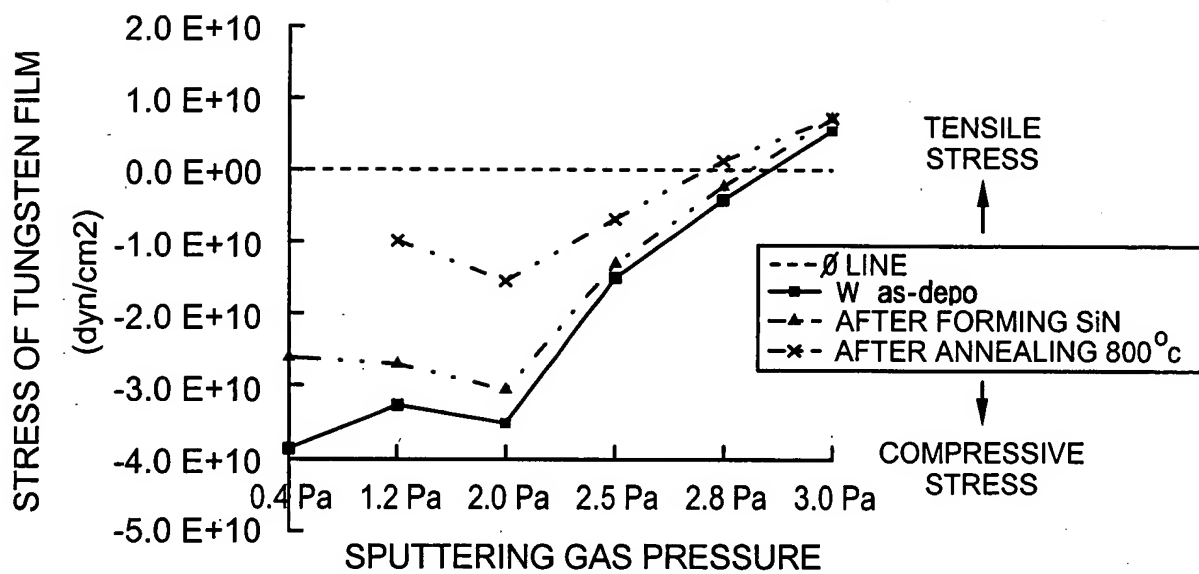


FIG. 23

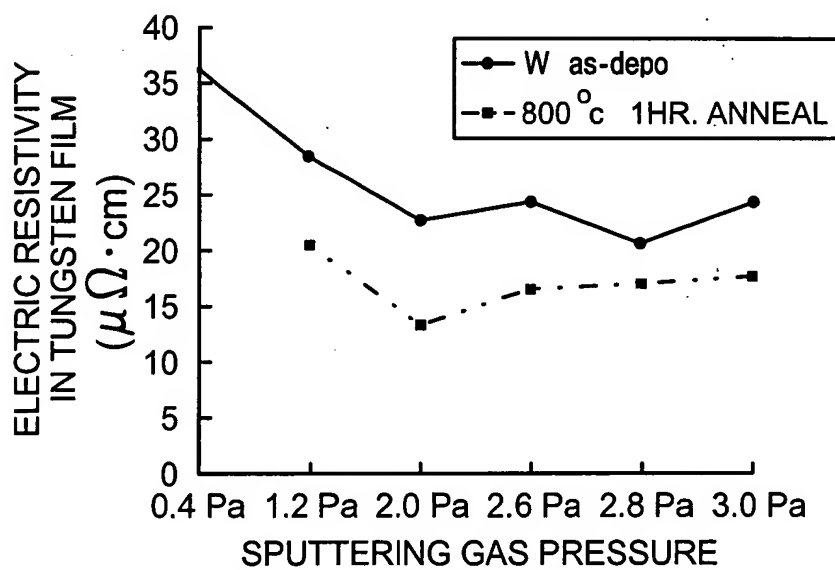
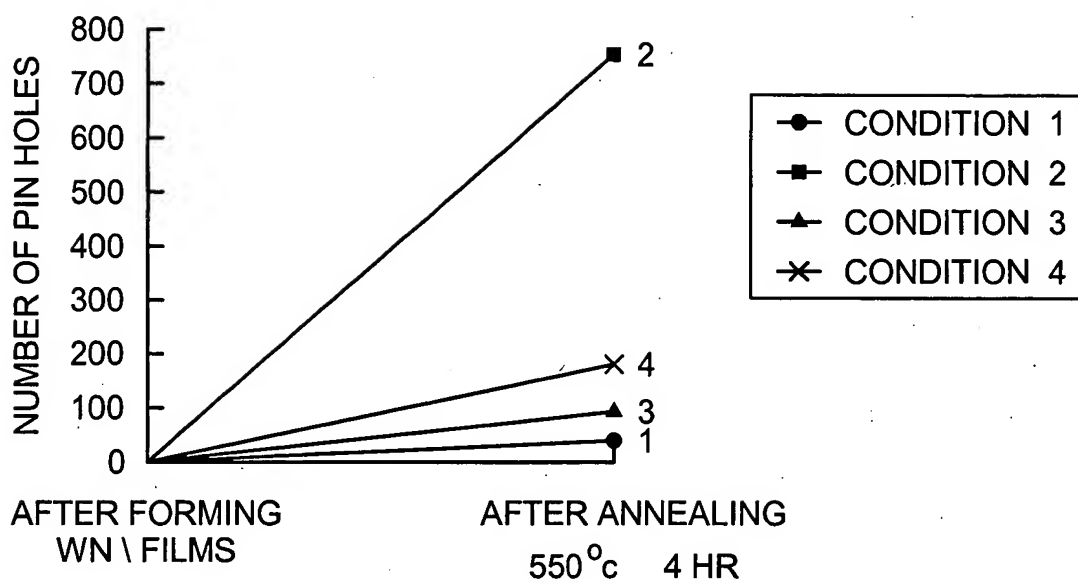


FIG. 24



*FIG. 25*

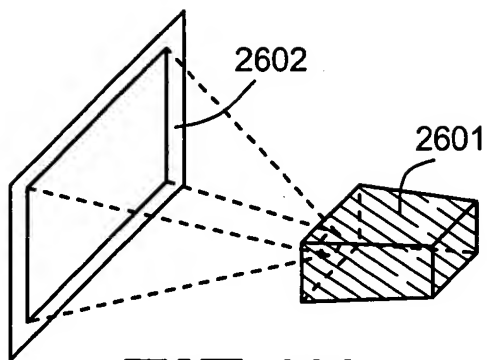


FIG. 26A

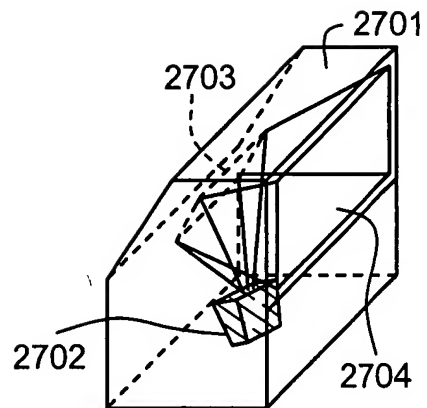


FIG. 26B

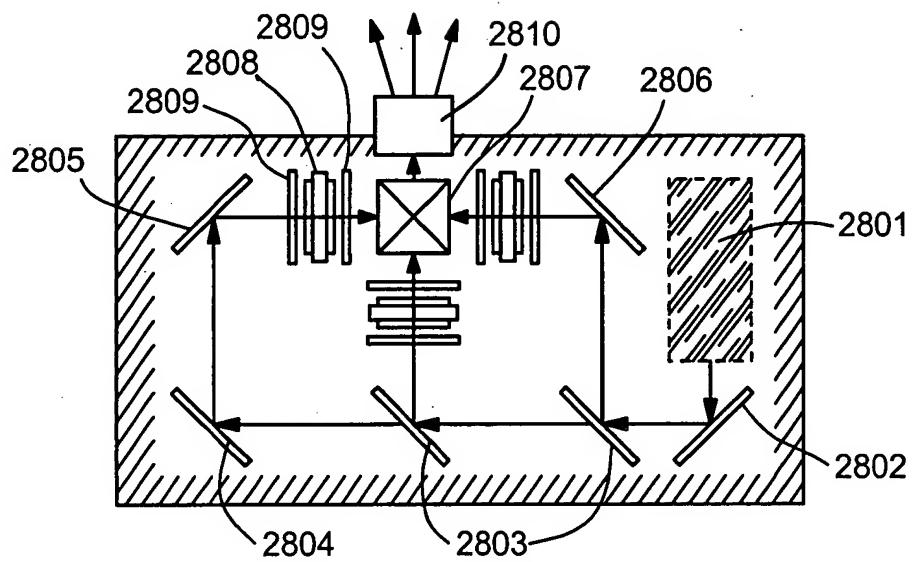


FIG. 26C

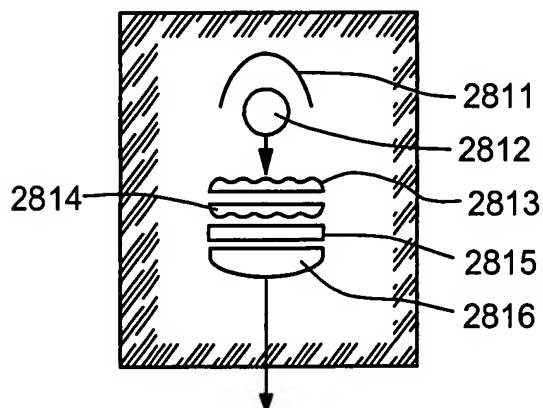


FIG. 26D

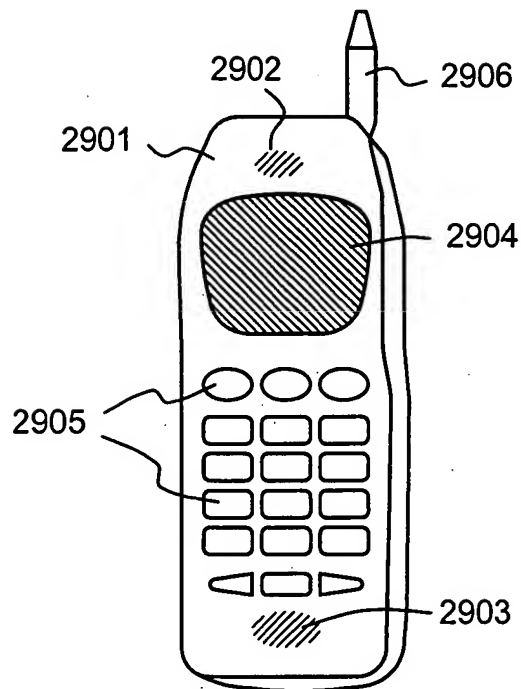


FIG. 27A

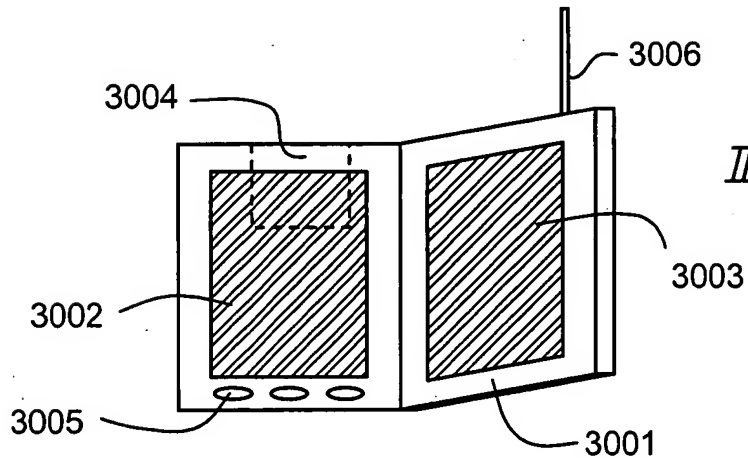


FIG. 27B

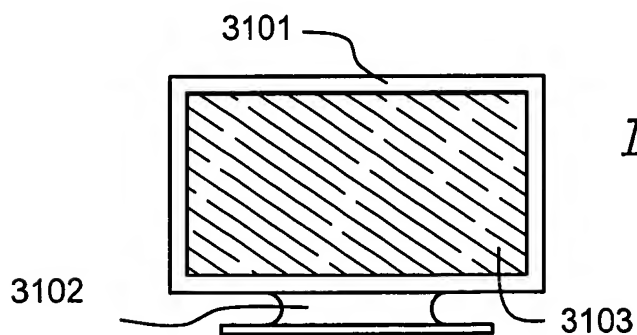


FIG. 27C